

REMARKS

Upon Entry of this amendment, claim 3 will be cancelled and thus claims 1-2, 5-13, 15-25, and 27-30 will remain in the application. Entry of this amendment is respectfully requested.

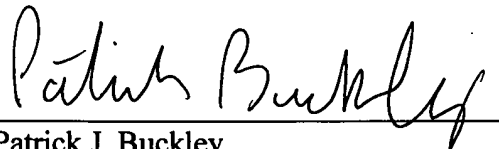
As illustrated in FIG. 4 of the present application, configurable mask bits 162 may be ANDed with configurable bits of a cache address 166 to generate set index bits 186. Moreover, inverted configurable mask bits 162 may be ANDed with the configurable bits of the cache address 166 to generate tag bits 178. To avoid any potential confusion, cancellation of claim 3 is respectfully requested.

CONCLUSION

Applicant respectfully requests that this amendment be entered before the grant of the patent. If any issues remain, the Examiner is kindly invited to contact the undersigned via telephone at (203) 972-0191.

September 13, 2006
Date

Respectfully submitted,



Patrick J. Buckley
Registration No. 40,928
Buckley, Maschoff & Talwalkar LLC
Attorneys for Intel Corporation.
Five Elm Street
New Canaan, CT 06840
(203) 972-0191